

SCS206AJHR SiC Schottky Barrier Diode

V _R	650V
I _F	6A
Q _C	9nC

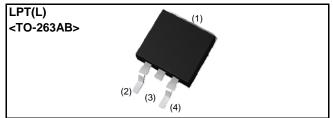
Features

Construction

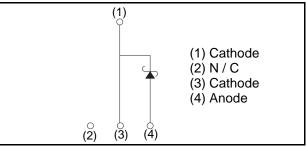
- 1) Shorter recovery time
- 2) Reduced temperature dependence
- 3) High-speed switching possible

AEC-Q101 Qualified

Outline



•Inner circuit



•Packaging specifications

Packaging	Embossed tape	
Reel size (mm)	330	
Tape width (mm)	24	
Basic ordering unit (pcs)	1,000	
Packing code	TLL	
Marking	SCS206AJ	
	Reel size (mm) Tape width (mm) Basic ordering unit (pcs) Packing code	

•Absolute maximum ratings (Ti = 25° C)

Silicon carbide epitaxial planer type

Parameter	Symbol	Value	Unit	
Reverse voltage (repetitive peak)	V _{RM}	650	V	
Reverse voltage (DC)	V _R	650	V	
Continuous forward current	I _F	6* ¹	А	
		24* ²	А	
Surge no repetitive forward current	I _{FSM}	91* ³	А	
		18* ⁴	А	
Repetitive peak forward current	I _{FRM}	25* ⁵	А	
Total power dissipation	P _D	48* ⁶	W	
Junction temperature	Тј	175	°C	
Range of storage temperature	Tstg	-55 to +175	°C	

*1 Tc=135°C *2 PW=8.3ms sinusoidal,Tj=25°C

*3 PW=10µs square,Tj=25°C *4 PW=8.3ms sinusoidal, Tj=150°C

*5 Tc=100°C,Tj=150°C,Duty cycle=10% *6 Tc=25°C

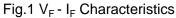
•Electrical characteristics (Tj = 25°C)

Deremeter	Symbol	Constitions	Values			11.2
Parameter		Conditions	Min.	Тур.	Max.	Unit
DC blocking voltage	V _{DC}	I _R =0.12mA	600	-	-	V
Forward voltage	V _F	I _F =6A,Tj=25°C	-	1.35	1.55	V
		I _F =6A,Tj=150°C	-	1.55	-	V
		I _F =6A,Tj=175°C	-	1.63	-	V
Reverse current	I _R	V _R =600V,Tj=25°C	-	1.2	120	μA
		V _R =600V,Tj=150°C	-	18	-	μA
		V _R =600V,Tj=175°C	-	42	-	μA
Total capacitance	С	V _R =1V,f=1MHz	-	219	-	pF
		V _R =600V,f=1MHz	-	22	-	pF
Total capacitive charge	Qc	V _R =400V,di/dt=350A/µs	-	9	-	nC
Switching time	tc	V _R =400V,di/dt=350A/µs	-	12	-	ns

•Thermal characteristics

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Thermal resistance	R _{th(j-c)}	-	-	2.3	3.1	°C/W

•Electrical characteristic curves



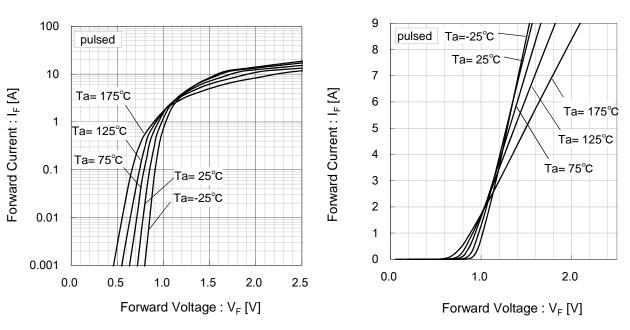
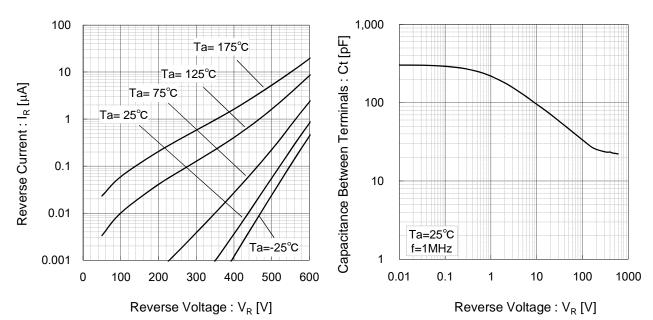


Fig.2 V_F - I_F Characteristics

Fig.3 V_R - I_R Characteristics

Fig.4 V_R-Ct Characteristics



•Electrical characteristic curves

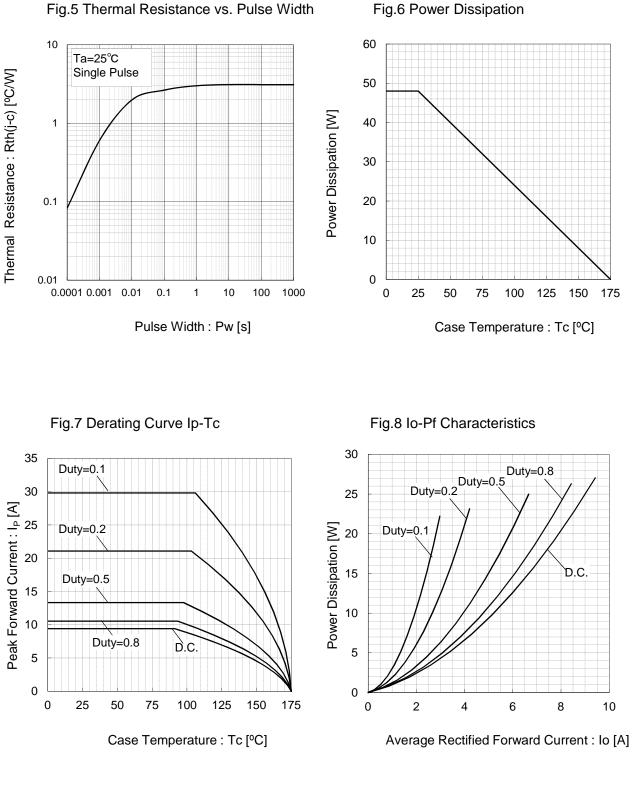


Fig.5 Thermal Resistance vs. Pulse Width

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